WHAT IS CLAIMED IS:

1. A method for implementing a scoreboard, comprising:

associating an instruction with an index value;

associating the instruction with a scoreboard entry corresponding to the index value;

receiving an indication that a terminating event associated with the instruction has occurred; and

invalidating the scoreboard entry.

2. The method of claim 1, wherein invalidating the scoreboard entry further comprises invalidating the scoreboard entry after the indication of a terminating event is received.

3. The method of claim 1, wherein:

the instruction is a load instruction; and

associating the instruction with a scoreboard entry corresponding to the index value further comprises associating the load instruction with a scoreboard entry corresponding to the index value.

4. The method of claim 3, wherein:

receiving an indication that a terminating event associated with the instruction has occurred further comprises receiving an indication that load data associated with the load instruction has been retrieved.

5. The method of claim 1, wherein invalidating the scoreboard entry further comprises:

using the index to identify the scoreboard entry corresponding to the instruction; and

invalidating the scoreboard entry corresponding to the instruction.

6. The method of claim 1 further comprises:

forwarding the instruction and the index value to a load/store processing unit.

7. The method of claim 1 further comprises:

receiving the index value from a load/store processing unit.

8. The method of claim 1, wherein the scoreboard entry is one of a plurality of scoreboard entries.

9. A computer system that provides indexed scoreboarding, comprising:

a main memory;

at least one processing unit coupled to the main memory;

a module, coupled to the main memory, that associates an instruction with an index value;

a module that associates the instruction with a scoreboard entry corresponding to the index value;

a module that is capable of receiving an indication that a terminating event associated with the instruction has occurred; and

a module that invalidates the scoreboard entry.

10. The computer system of claim 9, wherein the module that invalidates the scoreboard entry further comprises a module that invalidates the scoreboard entry after the indication of a terminating event is received.

11. The computer system of claim 9, wherein:

the module that associates an instruction with an index value further comprises a module that associates a load instruction with an index value; and

the module that associates the instruction with a scoreboard entry corresponding to the index value further comprises a module that associates the load instruction with a scoreboard entry corresponding to the index value.

12. The computer system of claim 9 further comprises:

a scoreboard coupled to the processor, the scoreboard having a plurality of scoreboard entry spaces.

13. The computer system of claim 11 wherein the module that is capable of receiving an indication that a terminating event associated with the instruction has occurred further comprises a module that is capable of receiving an indication that load data associated with the load instruction has been retrieved.

14. The computer system of claim 9, wherein the module that invalidates the scoreboard entry further comprises:

a module that utilizes the index to identify the scoreboard entry corresponding to the instruction; and

a module that invalidates the scoreboard entry corresponding to the instruction.

15. The computer system of claim 9 further comprises:

a module that forwards the instruction and the index value to a load/store processing unit.

16. The computer system of claim 9 further comprises:

a module that is capable of receiving the index value from a load/store processing unit.

17. A computer system that provides indexed scoreboarding, comprising:

a main memory;

at least one processing unit coupled to the main memory;

means, coupled to the main memory, for associating an instruction with an index value;

means for associating the instruction with a scoreboard entry corresponding to the index value;

means for receiving an indication that a terminating event associated with the instruction has occurred; and

means for invalidating the scoreboard entry.

18. The computer system of claim 17, further comprising:

means for forwarding the instruction and the index value to a load/store processing unit;

means for receiving the index value from the load/store processing unit; and

means for identifying the scoreboard entry based on the index value received from the load/store processing unit; and

means for invalidating the scoreboard entry after the indication of a terminating event is received.

19. The computer system of claim 17, further comprising:

a scoreboard having a plurality of scoreboard entry spaces.

20. The computer system of claim 17, wherein the instruction is a load instruction.